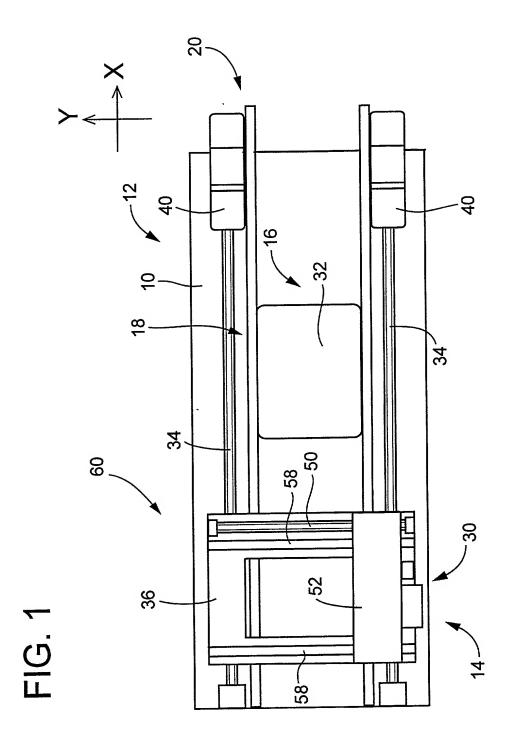
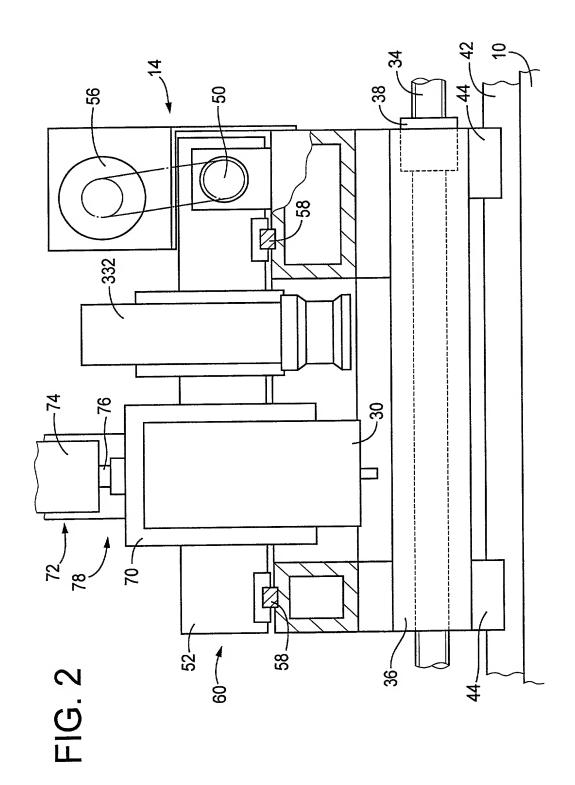
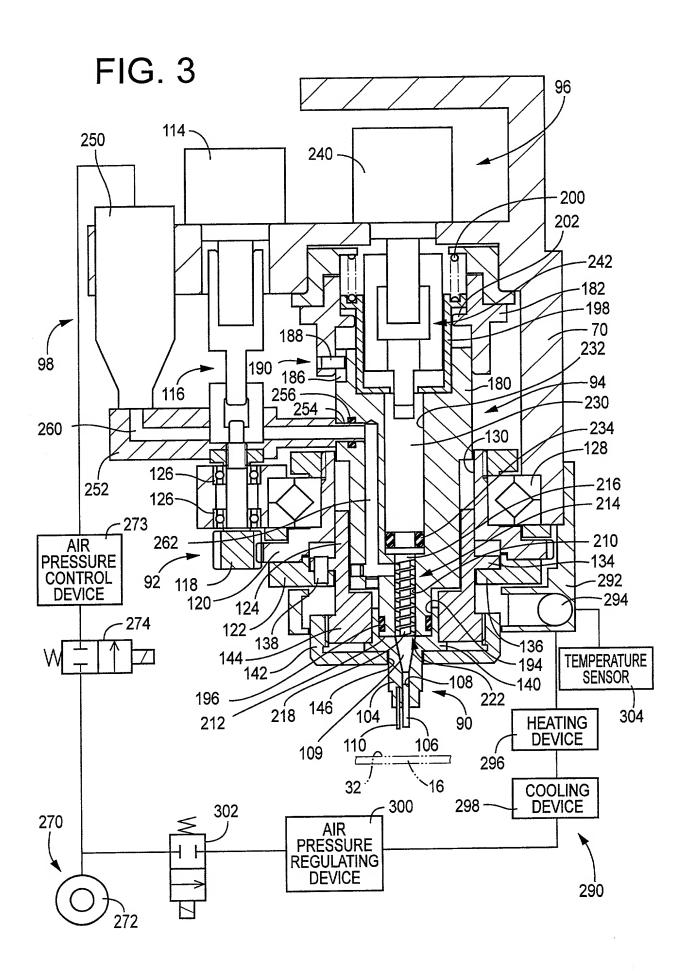
ŧş







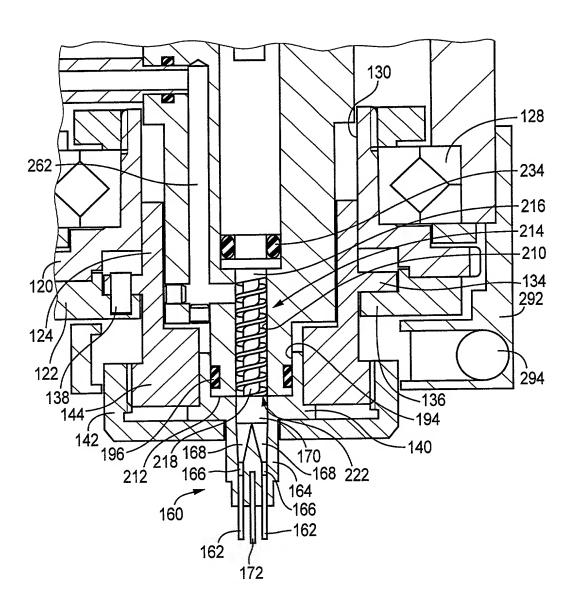
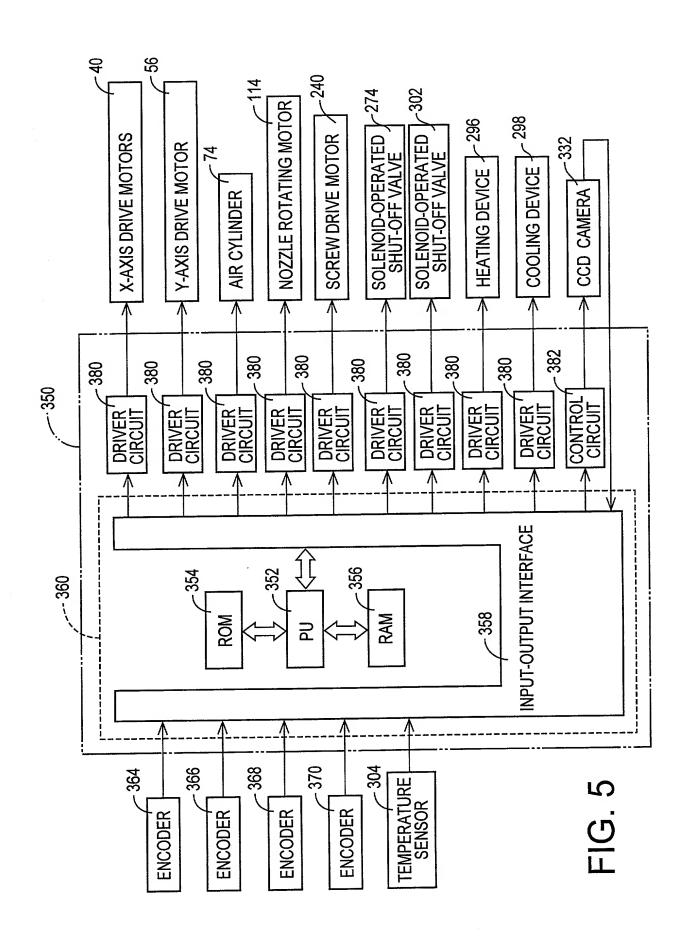


FIG. 4



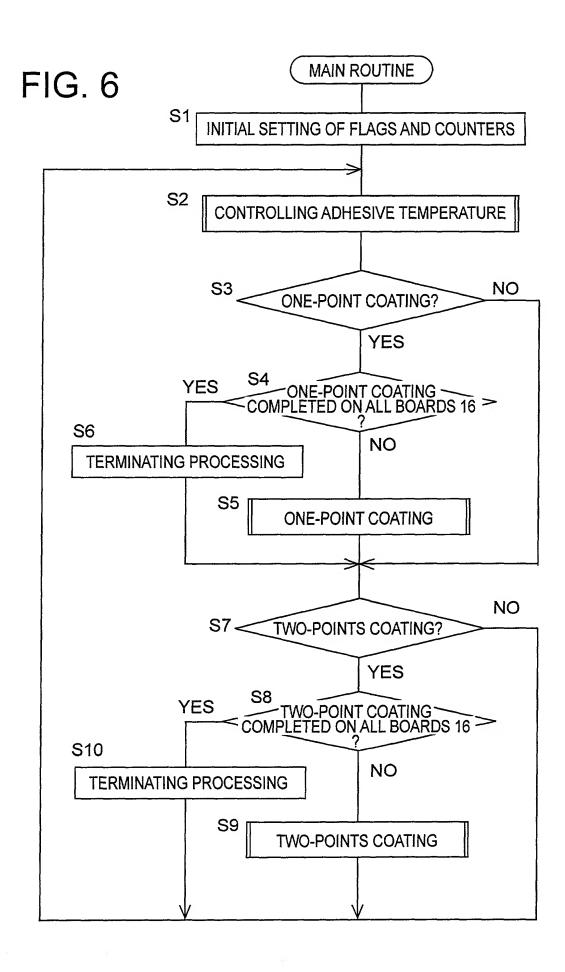


FIG. 7

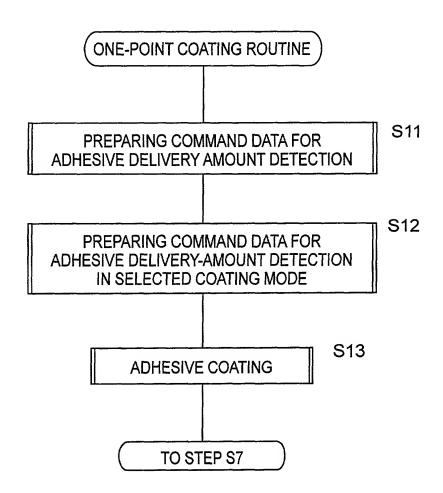
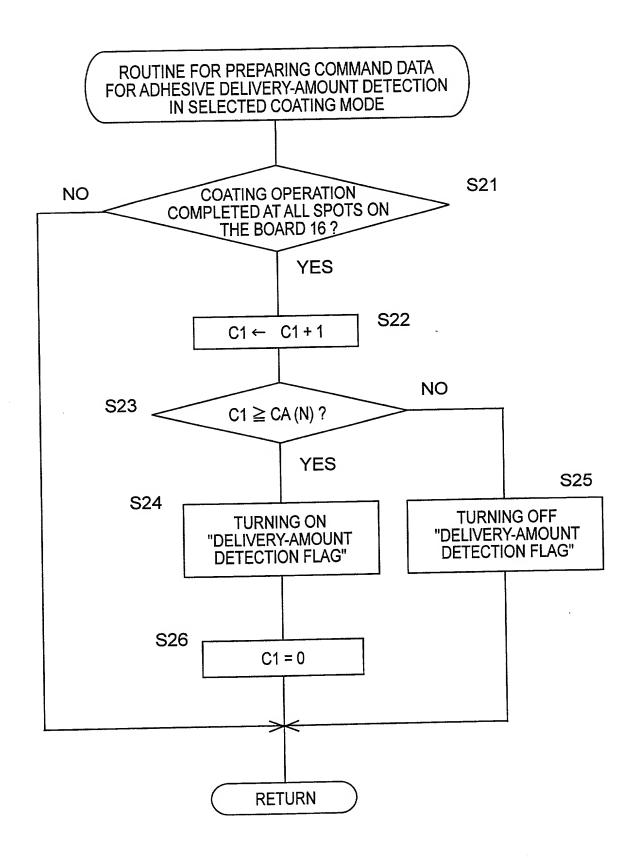
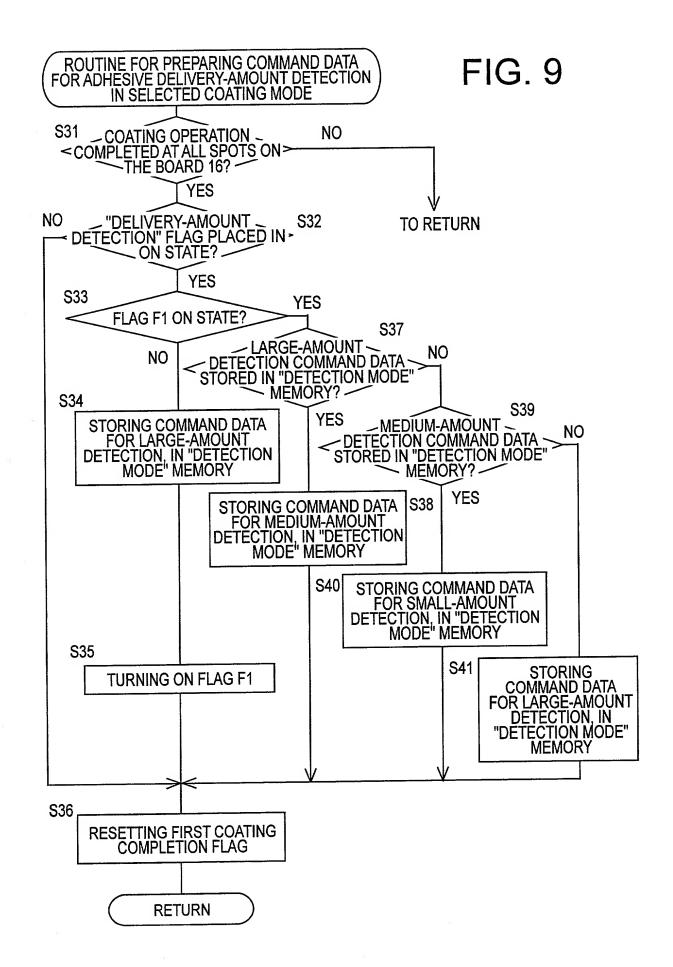
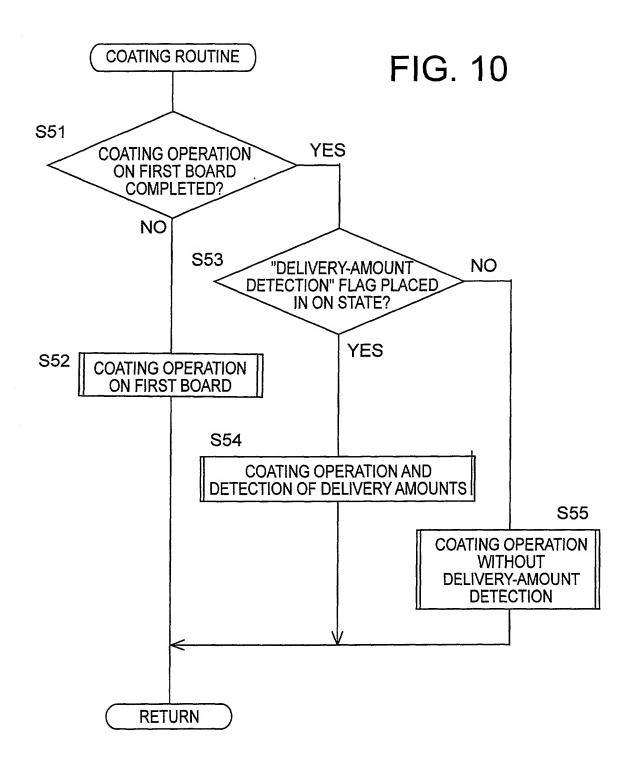
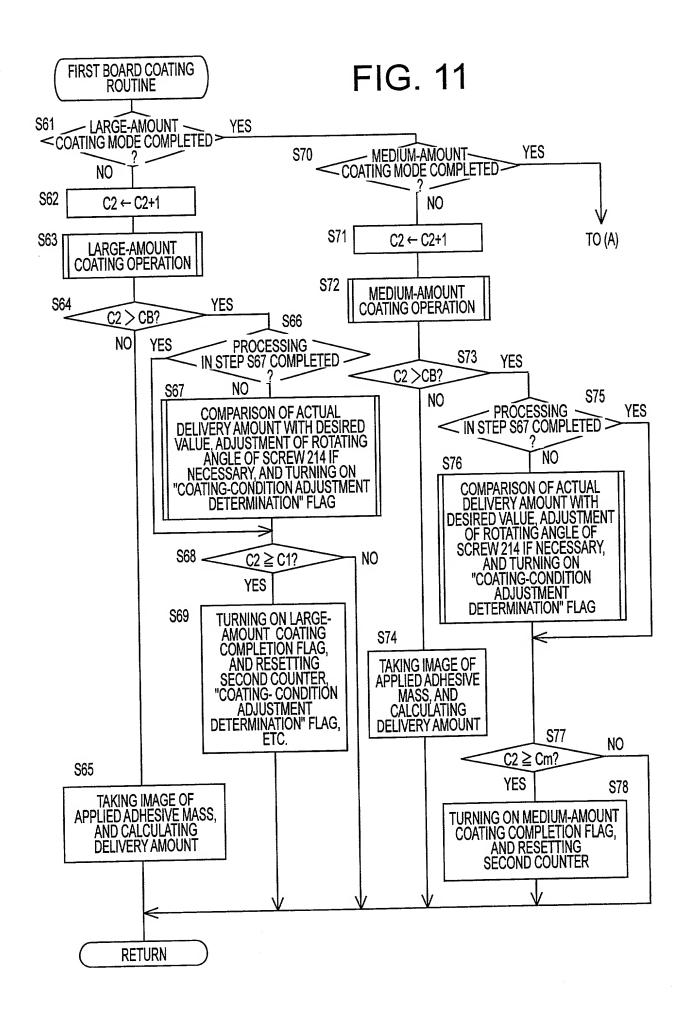


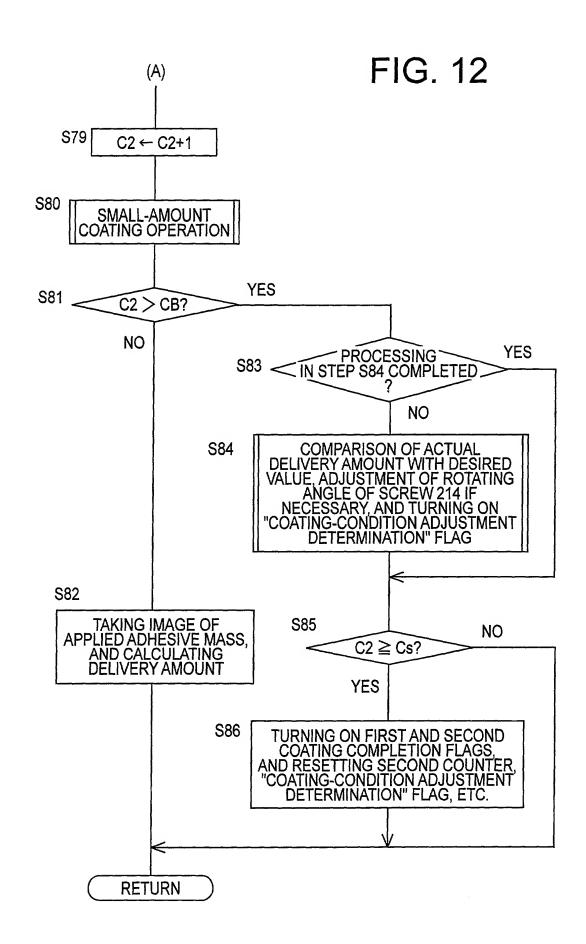
FIG. 8

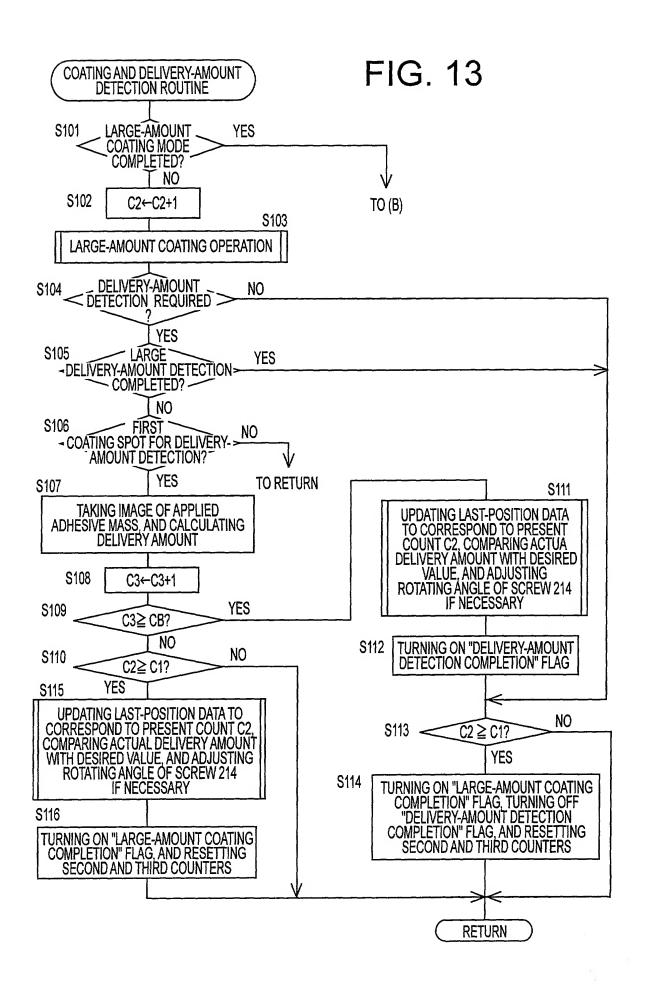


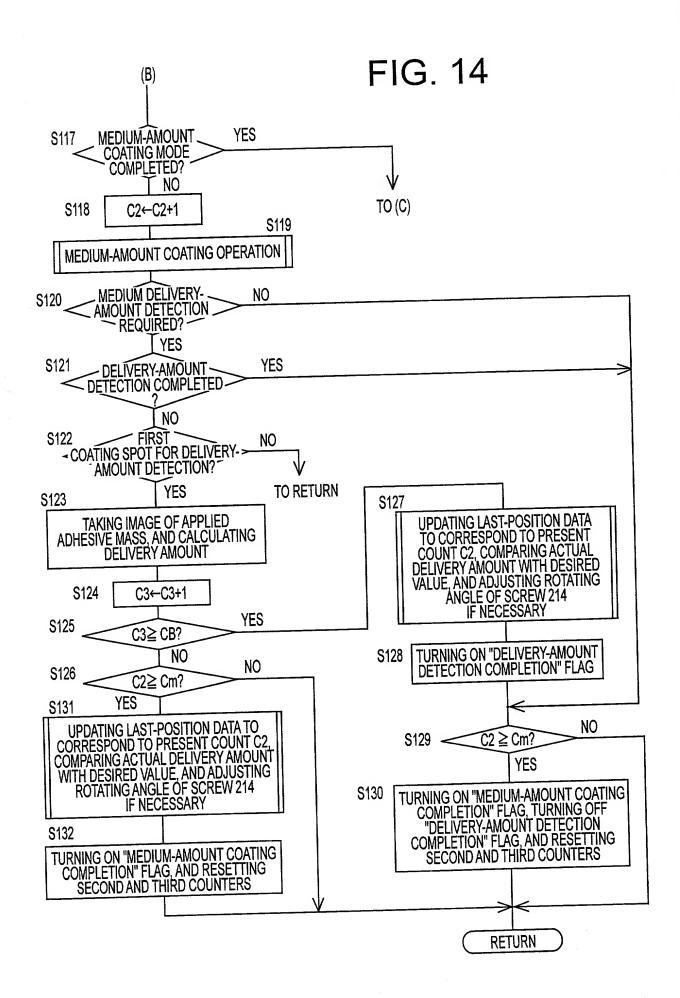


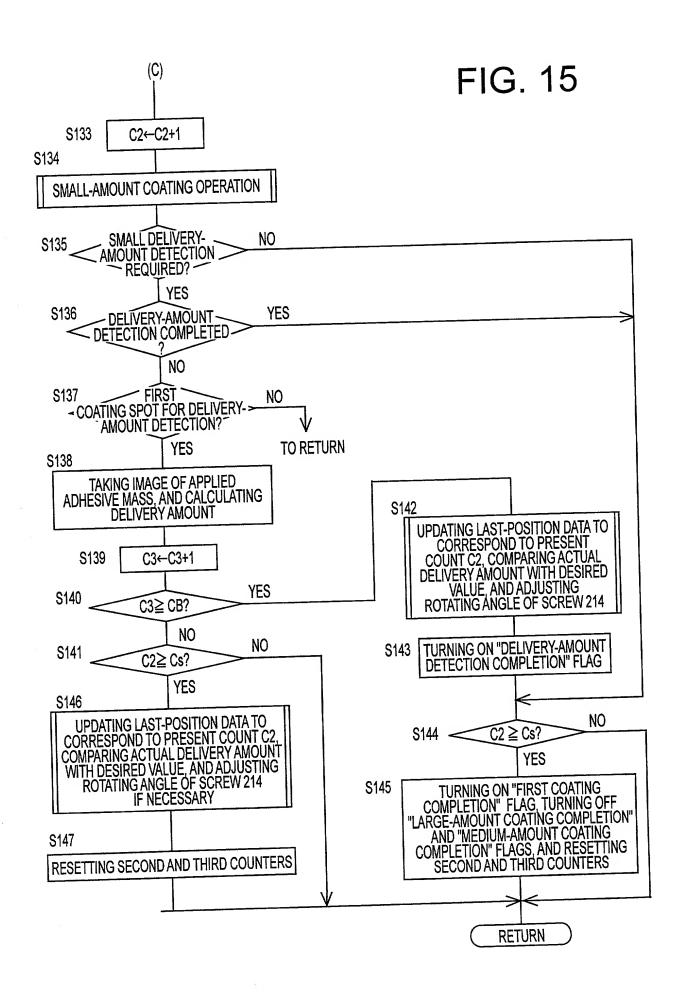












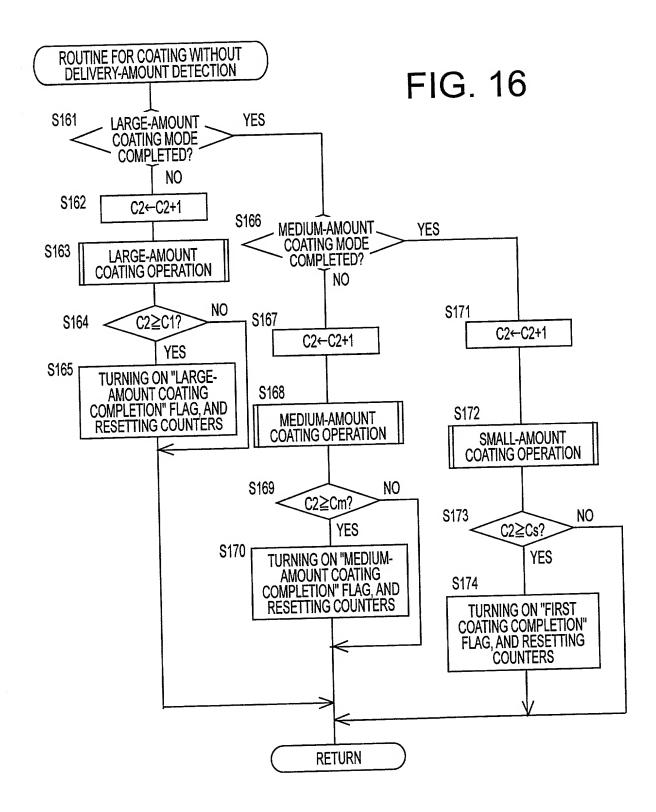


FIG. 17

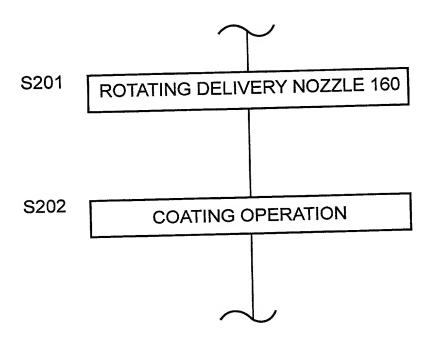


FIG. 18

356

RAM DELIVERY-AMOUNT DETECTION MODE CONTROL PROGRAM MEMORY **MEMORY** COATING POINT-NUMBER LARGE DELIVERY-AMOUNT MEMORY **MEMORY** MEDIUM DELIVERY-AMOUNT MEMORY DELIVERY AMOUNT MEMORY COATING CONDITION SMALL DELIVERY-AMOUNT MEMORY ADJUSTMENT DETERMINATION FLAG LAST LARGE-AMOUNT DETECTING DELIVERY-AMOUNT DETECTION POSITION MEMORY COMPLETION FLAG LAST MEDIUM-AMOUNT DETECTING FLAG F1 **POSITION MEMORY** LAST SMALL-AMOUNT DETECTING FIRST COUNTER **POSITION MEMORY** SECOND COUNTER FIRST COATING COMPLETION FLAG SECOND COATING COMPLETION FLAG THIRD COUNTER DELIVERY-AMOUNT DETECTION FLAG LARGE-AMOUNT COATING **COMPLETION FLAG** MEDIUM-AMOUNT COATING **COMPLETION FLAG**

FIG. 19

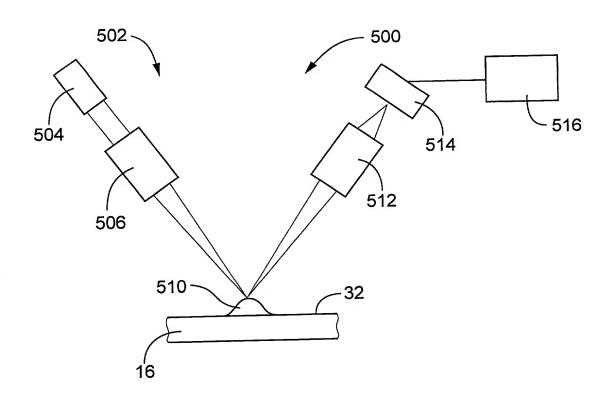


FIG. 20

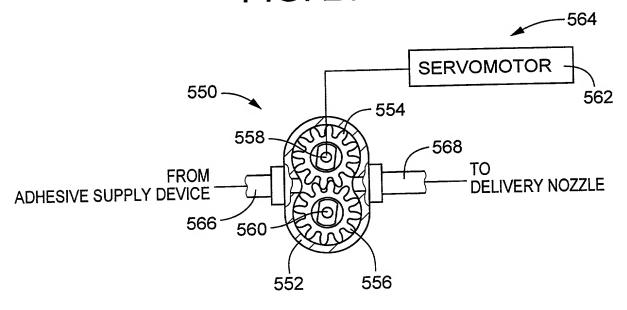


FIG. 21

